

IN THE CLAIMS

Please cancel claims 1, 3, 4, 6, 11-14, 16, 20-26, 28 and 29 in accordance with the following listing showing the status of all claims in the application.

1-6. (Canceled)

7. (Previously Presented) A circuit comprising:

a first circuit having a first input and a first output, wherein said first output includes a function of a signal at said first input and also includes a noise component resulting from noise experienced by said first circuit;

a second circuit having a second input and a second output;

a signal supplying circuit supplying to the second input a signal which is an inverse of the signal at the first input;

a third circuit having a third input connected to said first output and a fourth input connected to said second output, and subtracting said second output from said first output; and

a digital circuit proximal to said first circuit and to said second circuit.

8. (Previously Presented) A circuit according to claim 7 wherein said third circuit further comprises a halving circuit which inputs a signal having an input amplitude and outputs the signal at one-half the input amplitude.

9. (Canceled)

10. (Previously Presented) A circuit according to claim 7, wherein said first circuit, said second circuit, said third circuit, and said digital circuit are on a single integrated circuit chip.

11-26. (Canceled)

27. (Previously Presented) A circuit according to claim 7, wherein said second circuit is identical to said first circuit.

28-29. (Canceled)